



Lukas Graussam

Curriculum Vitae

Education

- 2021–Present **Master of Science**, *Computer Engineering*, Vienna University of Technology.
- 2017–2021 **Bachelor of Science**, *Computer Engineering*, Vienna University of Technology.
- 2011–2016 **Higher Technical College (HTL)**, *Electronic Engineering*, Hollabrunn.

Bachelor's Thesis

- Title *Automatic VHDL Verification Framework for Teaching Purposes*
- Description This thesis presents a novel system for automated verification of VHDL examples. Compared to similar projects, it provides higher security, scalability and portability.

Experience

Vocational

- 2018–Present **Software Developer**, SIEMENS, DIGITAL GRID, Vienna.
My main responsibility is automated reporting in large-scale customer projects. Besides designing reports, I create GUIs for visualization and support customers with presentations and templates. Finding the best solutions together with customers and automating as many processes as possible are the parts I enjoy most.

- o Primarily used tools:
 - Java, JavaScript
 - SQL (Oracle)
 - JasperReports
 - Linux, Bash, Python

- 2013, 2014 **Summer Intern**, MKE, Heidenreichstein.
Sensors and wiring for automation plants

Miscellaneous

- 2016–2017 **Mandatory Military Service**, *Österreichisches Bundesheer*.
After a military basic training I actually served as a waiter, which was a great experience.

Technical Skills

Programming PYTHON, JAVA, JAVASCRIPT, C, ASSEMBLY, VHDL, SQL
Systems Linux, Windows, Git(Lab), Docker
Theoretical Formal Reasoning, Model Checking, Distributed Algorithms

Languages

German **Native**
English **Fluent**
Russian **Basic**

Basic words and phrases only

Personal Interests

- Piano
- Volleyball
- Traveling
- Guitar
- Climbing